`include "list.svh"

module top();

intf vif();

ahb\_top dut(.hclk(vif.hclk),

.hresetn(vif.hresetn),

.enable(vif.enable),

.dina(vif.dina),

.dinb(vif.dinb),

.addr(vif.addr),

.wr(vif.wr),

.slave\_sel(vif.slave\_sel),

.dout(vif.dout)

);

initial begin

vif.hclk = 0;

vif.hresetn = 1;

vif.enable = 1'b0;

vif.dina = 32'd0;

vif.dinb = 32'd0;

vif.addr = 32'd0;

vif.wr = 1'b0;

vif.slave\_sel = 2'b00;

#5 vif.hresetn = 0;

#5 vif.hresetn = 1;

end

always #5 vif.hclk=~vif.hclk;

initial begin

$display("display from module top");

uvm\_config\_db#(virtual intf)::set(uvm\_root::get(),"\*","vif",vif);

end

initial begin

run\_test("mem\_test");

end

initial begin

$dumpfile("dump.vcd");

$dumpvars;

#2000 $finish();

end

endmodule